

For Immediate Release

Press Release

DVCon 2011 Keynote to Address How Advanced Verification Methodologies Are Taking Hold in Mainstream Design

Conference boasts 14 first-time exhibitors for record total of 33

Louisville, CO – February 3, 2011 – “From Volume to Velocity” is the theme of the 2011 Design and Verification Conference (DVCon) Keynote to be delivered by Wally Rhines, Chairman and CEO of Mentor Graphics. DVCon 2011 will be held February 28-March 3 at the DoubleTree Hotel in San Jose, California.

The Keynote will be delivered on Tuesday, March 1 beginning at 4:00pm in the Oak/Fir Ballroom. Rhines will discuss the state of verification past, present and future and look at how advanced techniques are taking hold in mainstream design. He will offer his insights on where verification needs to evolve to ensure continued improvement.

The conference will offer design and verification engineers a rich technical program that includes information on advanced technology, techniques, standards and methods. To view the complete program, visit <http://dvcon.org/techprog.html>.

In addition, underscoring its value, DVCon has a record number of exhibitors this year for attendees to browse and learn about the latest design and verification tools and solutions on the market.

The DVCon Expo will have a total of 33 exhibitors, including 14 first-timers. New exhibitors include: Agnisis Inc., Calypto Design Systems, Duolog Technologies, Forte Design Systems, Innovative Logic, Inc., NextOp Software, Inc., Oski Technology, PerfectVIPs, S2C Inc., Semifore Inc., Severity1, Sibridge Technologies, Synapse Design Automation, and Vennsa Technologies, Inc. Exhibitors will be in the Bayshore Ballroom on Tuesday, March 1 from 2:00pm – 6:30pm and Wednesday, March 2 from 2:00 – 6:30pm. To see a complete list of exhibitors, visit <http://www.dvcon.org/exlist.html>.

To register for DVCon, visit <http://www.dvcon.org/reg.html>.

About DVCon

DVCon is the premier conference for discussion of the functional design and verification of electronic systems. DVCon is sponsored by **Accellera**, an industry consortium dedicated to the development and standardization of design and verification languages. For more information about Accellera, please visit www.accellera.org. For more information about DVCon, please visit www.dvcon.org. Follow @dvcon on Twitter or to comment, please use #dvcon.

For more information, please contact:

Kathy Embler
MP Associates, Inc.
303-530-4562
kathy@mpassociates.com

Barbara Benjamin
HighPointe Communications
503-209-2323
barbara@hipcom.com