

Press Release

DVCon 2012 Announces Final Program & Industry Leaders Panel Lineup

Louisville, CO – February 14, 2012 – The 20th annual Design and Verification Conference (DVCon), sponsored by Accellera Systems Initiative, announced today its final program as well as the speakers for the Industry Leaders Panel. DVCon 2012 will be held February 27-March 1 at the DoubleTree Hotel in San Jose, California.

Moderated by JL Gray, vice president at Verilab and author of “Cool Verification,” the Industry Leaders Panel will be held Tuesday, February 28th at 2:30pm in the Oak/Fir Ballroom. Titled, “The Resurgence of Chip Design,” Gray has lined up Ted Vucurevich, Enconcert, Inc., John Costello, Altera Corp., Gary Smith, Gary Smith EDA and Jim Hogan, Vista Ventures LLC to discuss the trends influencing the change.

A second panel, “Build or Buy: Which is the Best Practice for Hardware-Assisted Verification?” will be moderated by Brian Bailey of Brian Bailey Consulting and will be held on Wednesday, February 29th at 3:30pm in the Oak/Fir Ballroom.

The 2012 keynote address, “Systemic Collaboration: Principles for Success in IC Design,” will be presented by Aart de Geus, Chairman and CEO of Synopsys, Inc. at 2:00pm on February 29th in the Oak/Fir Ballroom.

Focusing on EDA and IP standards, Accellera Systems Initiative Day will be held on Monday, February 27th at the DoubleTree Hotel. For more information, visit http://www.accellera.org/news/events/dvcon_2012.

With more than 30 registered exhibitors, the DVCon Expo will be open on Tuesday from 3:30-6:30pm and Wednesday from 4:30-7:00pm. Poster sessions will be held on Tuesday from 10:30-11:30am and Wednesday from 10:00-11:00am.

To view the complete four day program, including all of the technical sessions and sponsored tutorials, please visit the conference website www.dvcon.org.

About DVCon

DVCon is the premier conference for discussion of the functional design and verification of electronic systems. DVCon is sponsored by **Accellera Systems Initiative**, an industry consortium dedicated to the development and standardization of design and verification languages. For more information about Accellera, please visit www.accellera.org. For more information about DVCon, please visit www.dvcon.org. Follow @dvcon on Twitter or to comment, please use #dvcon.

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