

# CALL FOR SHORT WORKSHOPS

## DVCON US 2020

March 2 - 5, 2020 • DoubleTree Hotel, San Jose, CA

DVCon is the premier conference on the application of languages, tools, and methodologies for the design and verification of electronic systems and integrated circuits. The focus of the conference is the usage of specialized design and verification languages such as Verilog, SystemVerilog, VHDL, PSL, SystemC and e, as well as general purpose languages such as C, C++, PERL, Tcl, and Python. Tools and methodologies include the use of machine learning, testbench automation, portable stimulus, hardware-assisted verification, hardware/software co-verification, formal verification, transaction-level system design, high level synthesis, low power design techniques, 3D chip designs, IP based SoC design methods, reference flows and AMS design.

DVCon offers short workshops to encourage greater sponsorship participation from companies and exhibitors, especially smaller organizations at an affordable level.

DVCon is seeking for short workshop topics that are current, have a high-level of interest and offer strong continuing educational content.

Short Workshop sponsors reach a captive audience during the 90-minute educational sessions and have the opportunity to follow-up with attendees during breaks, at the exhibits, and following the event.

DVCon is a highly-targeted venue for engineers addressing major design and verification issues. Submit proposals by **Tuesday, September 24.**

[Submit Here](#)

### ***DVCON SPONSORED SHORT WORKSHOP WITH RECEPTION: \$8,125***

#### **Sponsorship Includes:**

- A 90 minute Short Workshop presentation on Monday or Thursday
- Short Workshop content will be publicized via monthly newsletters, DVCon website, Conference Program and in the Opening Session
- Promotional email sent announcing your workshop and reception
- Other promotion items like banners, flyers, gift items, etc. can be distributed during the Short Workshop
- 1 complimentary one-day conference registration for the Short Workshop
- Exclusive sponsor of an evening reception on the exhibit floor which includes:
  - Opportunity to create a signature drink
  - Customized meter board displayed during the day of the reception (you create the graphic of your choice and DVCon will produce)

#### **Your company logo and sponsorship status recognized on:**

- |   |   |
|---|---|
| a) Reception signage  | c) Reception signage at each bar location |
| b) Reception page in the Conference Program / Exhibit Guide | d) Web page                               |

### ***DVCON SPONSORED SHORT WORKSHOP: \$3,050***

#### **Sponsorship Includes:**

- A 90 minute Short Workshop presentation on Monday or Thursday
- Short Workshop content will be publicized via monthly newsletters, DVCon website, Conference Program and in the Opening Session presentation slides
- Promotional email sent announcing your workshop
- Other promotion items like banners, flyers, gift items, etc can be distributed during the Short Workshop
- 1 complimentary one-day conference registration for the Short Workshop

For more information concerning the conference, please contact the conference management, Nannette Jordan at [Nannette@mpassociates.com](mailto:Nannette@mpassociates.com)

# CALL FOR SHORT WORKSHOPS

## DVCON US 2020

March 2 - 5, 2020 • DoubleTree Hotel, San Jose, CA

### SHORT WORKSHOP PROPOSAL REQUIREMENTS

**Deadline: Tuesday, September 24**

DVCon short workshops are open to all attendees and are included in the full conference registration. Please include in the proposal the name of the companies that will be sponsoring the short workshop.

- Attendee expectations are high regarding currency of topic, depth of engineering content and breadth of real-life examples
- The Tutorial/ Short Workshop Chair will review final presentation materials to ensure high quality educational content
- Include suggested presenters names, affiliations and biographies.
- Your proposal should be a short abstract of the short workshop, two to five paragraphs, 1,000 words maximum
- Presentation slides need to be supplied in an electronic format in advance of the conference. Presentation slides will be distributed to the attendees in electronic format. Hard copies will not be provided
- Please indicate if this short workshop is a “hands-on” session or lecture format
- Any necessary additional hardware that you may require must be provided by the short workshop organizers

### SUGGESTED TOPICS

- SystemVerilog for Verification and/or Design
- SystemVerilog for Verification and/or Design
- SystemC /C/C++ Design and/or Verification of systems.
- SoC and Software-driven Verification
- Assertion-based Verification. SystemVerilog Assertions, PSL, etc.
- Coverage-driven Verification
- High-level Synthesis
- Low-power Design and Verification techniques
- Secure/Encrypted IP-based SoC design methods
- Debug for design and verification
- Mixed-signal modeling and verification
- Transaction Level Modeling (TLM), ESL Design, and IP integration (IP-XACT)
- Functional Safety
- Security
- Embedded software verification
- Hardware/Software Co-development
- Verification Productivity Methods
- Formal Methodology and Static Analysis
- Emulation
- Post SI Debug
- FPGA Prototyping
- Moving from proprietary solutions to standards-based design and verification
- Portable Stimulus
- Application-specific design verification challenges, techniques Machine Learning driven techniques

### SHORT WORKSHOP DEADLINES

**September 24, 2019:** Proposals due. Submit at [DVCon.org](http://DVCon.org)

**November 5, 2019:** Accept/Reject notification

**November 28, 2019:** All Short Workshop content due for Conference Program and website: tutorial title, abstract, speaker names, affiliations and biographies

**January 7, 2020:** Draft Presentation slides due to DVCon Tutorial Chair

**January 14, 2020:** Presentation feedback due to presenters on slides

**February 11, 2020:** Final slides due for final production for attendee distribution

### CONFERENCE SCHEDULE

**Monday, March 2**

- Accellera Day Tutorials
- Short Workshops
- Exhibits

**Tuesday, March 3**

- Technical Sessions
- Keynote Speaker
- Exhibits

**Wednesday, March 4**

- Technical Sessions
- Panel Discussion
- Exhibits

**Thursday, March 5**

- Tutorials
- Short Workshops

SPONSORED BY: Accellera Systems Initiative is an industry consortium with a mission to provide design and verification standards required by systems, semiconductor, IP, and design tool companies to enhance a front-end design automation process. [Accellera.org](http://Accellera.org)

Conference Sponsor:



**General Chair**

Aparna Dey - Cadence Design Systems, Inc.  
[aparna@cadence.com](mailto:aparna@cadence.com)

**Tutorial/Short Workshop Chair**

Ambar Sarkar - NVIDIA Corp.  
[ambars@nvidia.com](mailto:ambars@nvidia.com)