

1. Which is your primary design language? (Pick one)		7. What on-chip buses do you intend to use in the next 12 months?	
16% Verilog		20% AMBA 2.0 AHB/APB	
3% VHDL		23% AMBA 3 AXI	
8% C/C++		4% OCP 2.0	
4% SystemC		4% OCP 2.1	
52% SystemVerilog		3% CoreConnect	
17% Not Applicable		18% Others/Proprietary	
		28% None	
2. Which primary verification language do you use? (Pick one)		8. What interfaces standards do you expect to use in the next 12 months?	
10% C/C++		6% PCI Express 1.1	
2% e		16% PCI Express 2.0	
2% OpenVera		7% USB 2.0/OTG	
15% Verilog		14% USB 3.0	
2% VHDL		6% Serial ATA	
4% SystemC		8% 10G Ethernet	
49% System Verilog		11% 10/100/1G Ethernet	
16% Not Applicable		4% Wireless USB	
		5% PCI/PCI-X	
3. Which primary verification language do you plan to use for your next design? (Pick one)		1% CE-ATA	
7% C/C++		22% None	
2% e			
1% OpenVera		9. What is the size in gates of your current/last design? (Pick one)	
9% Verilog		39% Not Applicable	
2% VHDL		9% <1M	
5% SystemC		8% 1 - 3M	
56% SystemVerilog		4% 3 - 5M	
18% Not Applicable		7% 5 - 10M	
		15% 10M - 50M	
4. Which verification methodology is used by your functional verification team?		18% >50M	
43% UVM			
8% OVM		10. How many clock domains do your designs average?	
5% VMM		5% 1	
0% eRM		5% 2	
4% SystemC/TLM		20% 2 - 5	
11% Home Grown		13% 5 - 10	
5% Other		11% 10 - 20	
24% I am not involved		11% >20	
		35% Not Applicable	
5. Which primary property specification (assertion-based verification) language do you use?			
26% Verilog		11. What is your number one design constraint?	
6% VHDL		47% Low power	
5% PSL		14% Size/density	
63% SystemVerilog (SVA)		39% Performance/throughput	
6. What area(s) are you focused on? (Check all that apply)		12. What are the two main reasons for your attendance at DVCon?	
9% Systems Design		17% Learn new techniques to improve your design process	
4% Standard ICs		27% Learn new methodologies to improve your verification process	
14% ASICs		18% Learn about new developments in design tools	
3% DSP Design		22% Meet and network with other engineers in the industry	
6% Microprocessor/Microcontroller Design		16% Learn about industry in general	
8% FPGAs & PLDs			
2% Multi-Chip Modules		13. Which category most closely describes your job description? (Pick one)	
2% PCBs		13% Senior Management	
2% Library Development		17% Engineering Management	
4% Analog/Mixed Signal		13% Design Engineer	
13% EDA Tools		3% System Architecture	
16% Verification		4% Application Engineer	
12% SOCs		4% Marketing	
5% Software/Embedded Software		4% Technical Marketing	
		2% Product Marketing	
		3% Sales	
		3% Research/Academic	
		3% CAD	
		27% Verification Engineer	
		4% Software Engineer	